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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/734,072  
Confirmation No. .... 8908  
Filing Date ..... December 10, 2003  
Inventor ..... Mark E. Tuttle et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2891  
Examiner ..... Caridad Everhart  
Customer No. .... 021567  
Attorney Docket No. .... MI40-369  
Title: Methods for Forming Integrated Circuits Within Substrates

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**  
References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §§ 1.56, 1.97 and 1.98. No admission is made regarding whether the listed references are prior art.


Citation of these references is respectfully requested.

Respectfully submitted,

Dated: \_\_\_\_\_

3/30/07

By: \_\_\_\_\_

  
James D. Shaurette  
Reg. No. 39,833

